

ABSTRACT OF THE DISCLOSURE

An improved memory device is operable in a plurality of protocols. The improved memory device has an interface circuit which receives communication signals from a communication bus. The interface circuit decodes the communication signals and generates a plurality of protocol signals and outputs one of the plurality of protocol signals in response to a select signal. A user selectable nonvolatile memory or fuse stores user selected protocol and generates the select signal corresponding to the user selected protocol. The memory device further comprises a nonvolatile memory and a controller for controlling the nonvolatile memory. The controller is responsive to the one protocol signal that is selected.